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**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
**MEARS ET AL.**

Serial No. **10/717,370**

Confirmation No. **6723**

Filing Date: **NOVEMBER 19, 2003**

For: **METHOD FOR MAKING SEMICONDUCTOR**  
**DEVICE INCLUDING BAND-**  
**ENGINEERED SUPERLATTICE**

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) Examiner R. Kunemund  
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) Art Unit: 1722  
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) Attorney Docket:  
) 62603\_CON2  
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**RESPONSE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of April 5, 2005,  
please enter the amendments and remarks set out below.